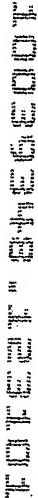
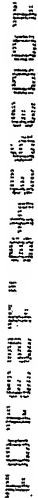


103946

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FIG. 3A 300

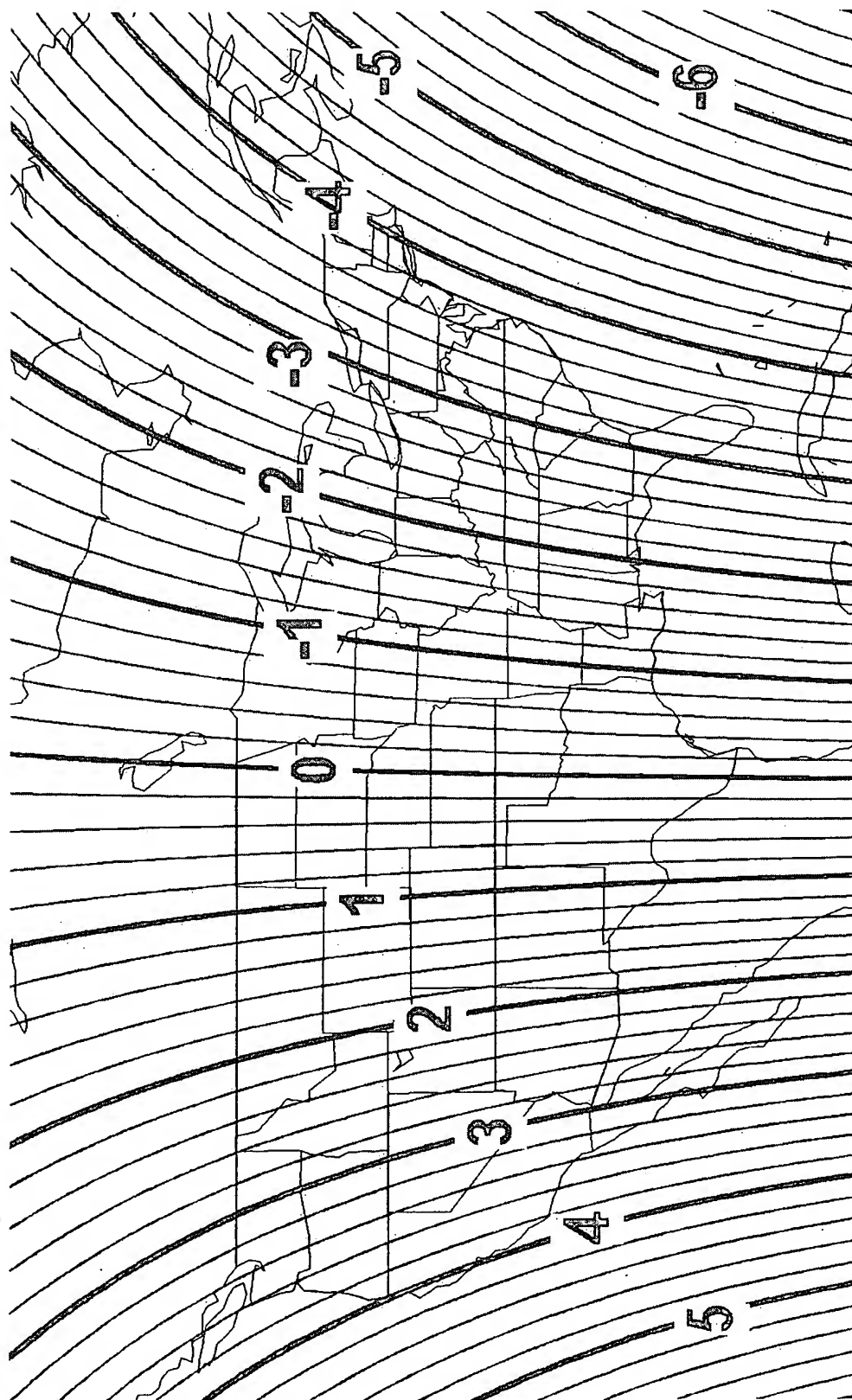


FIG. 3B

301

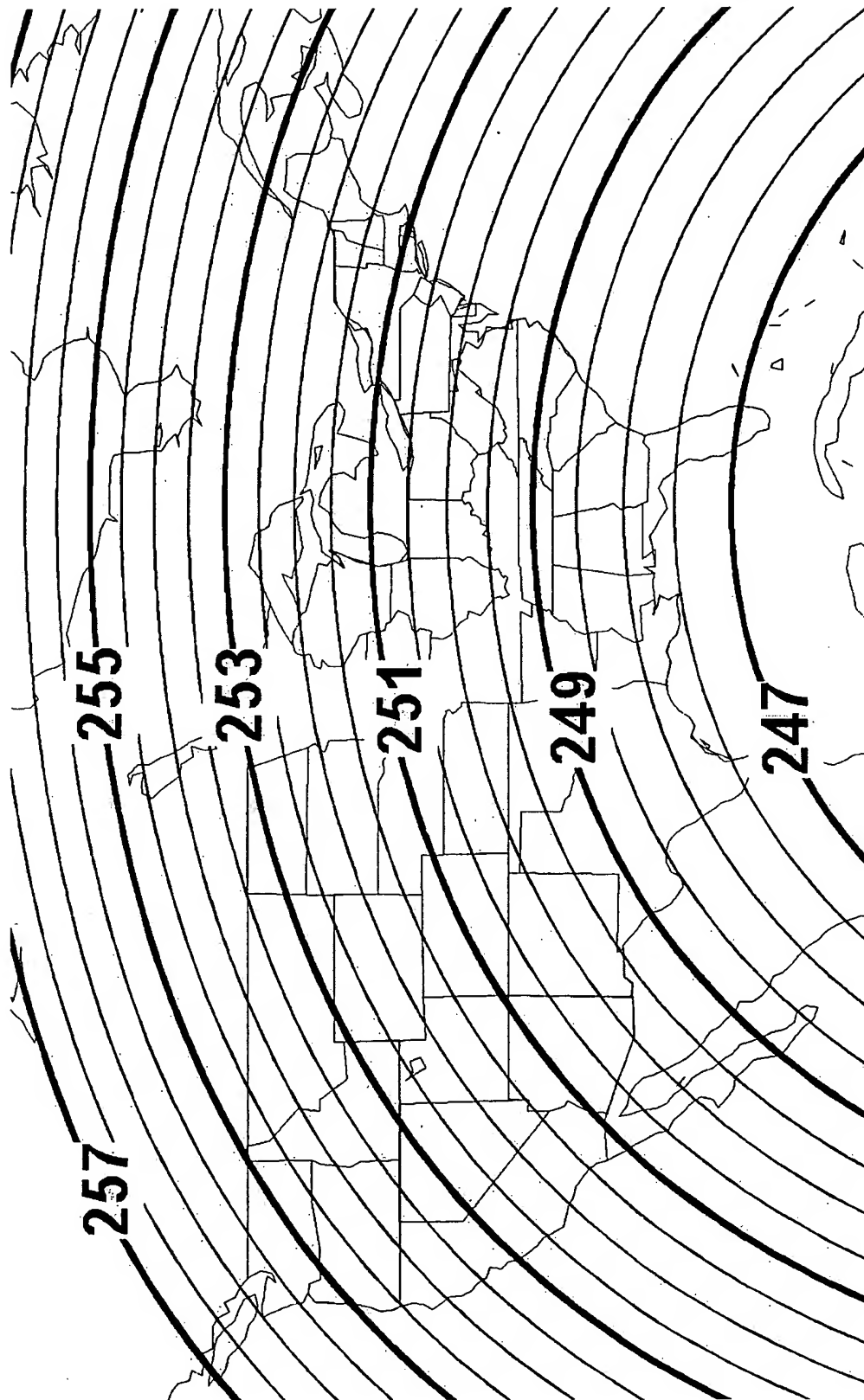


FIG. 3C 350

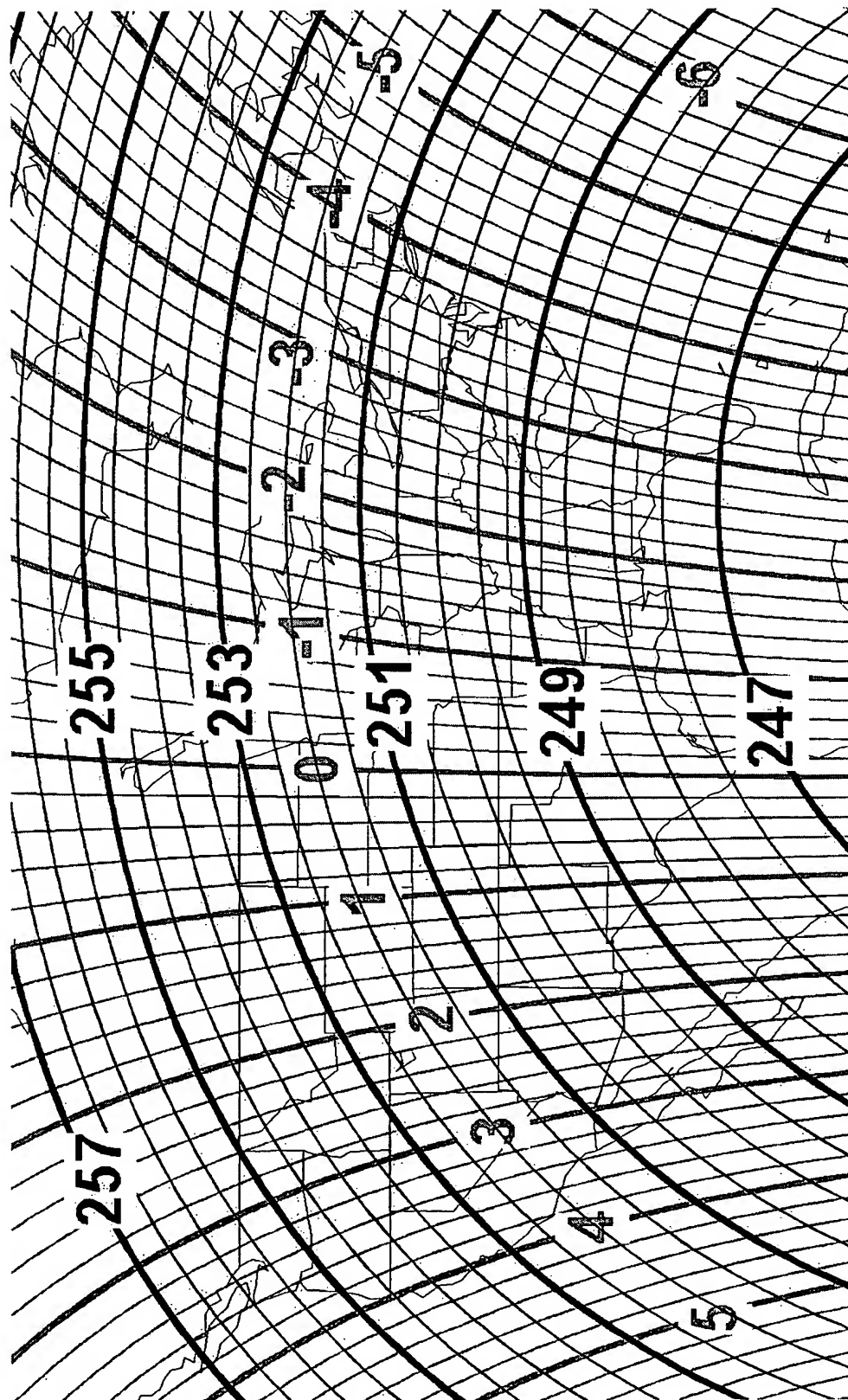


FIG. 3D

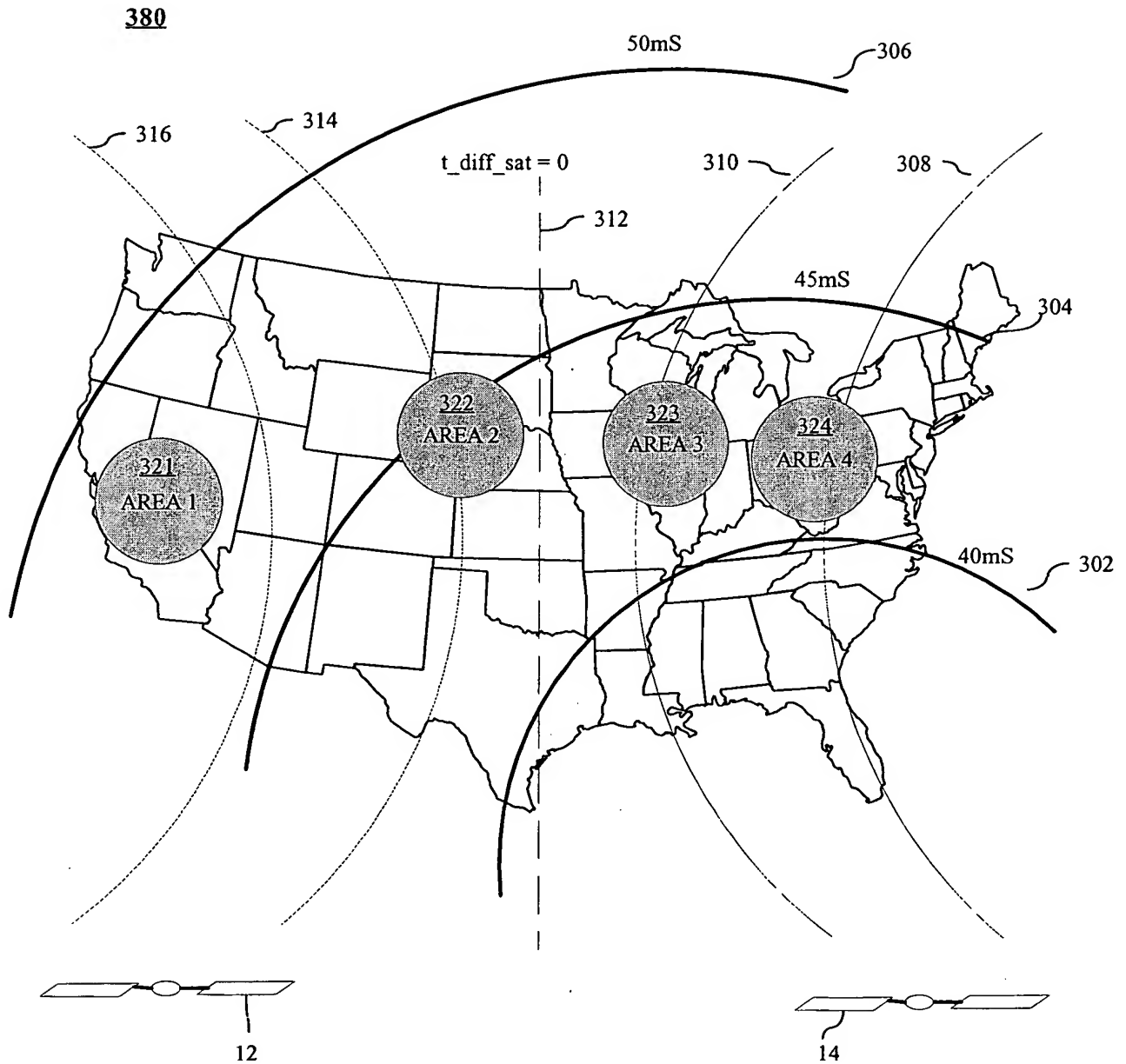


FIG. 4

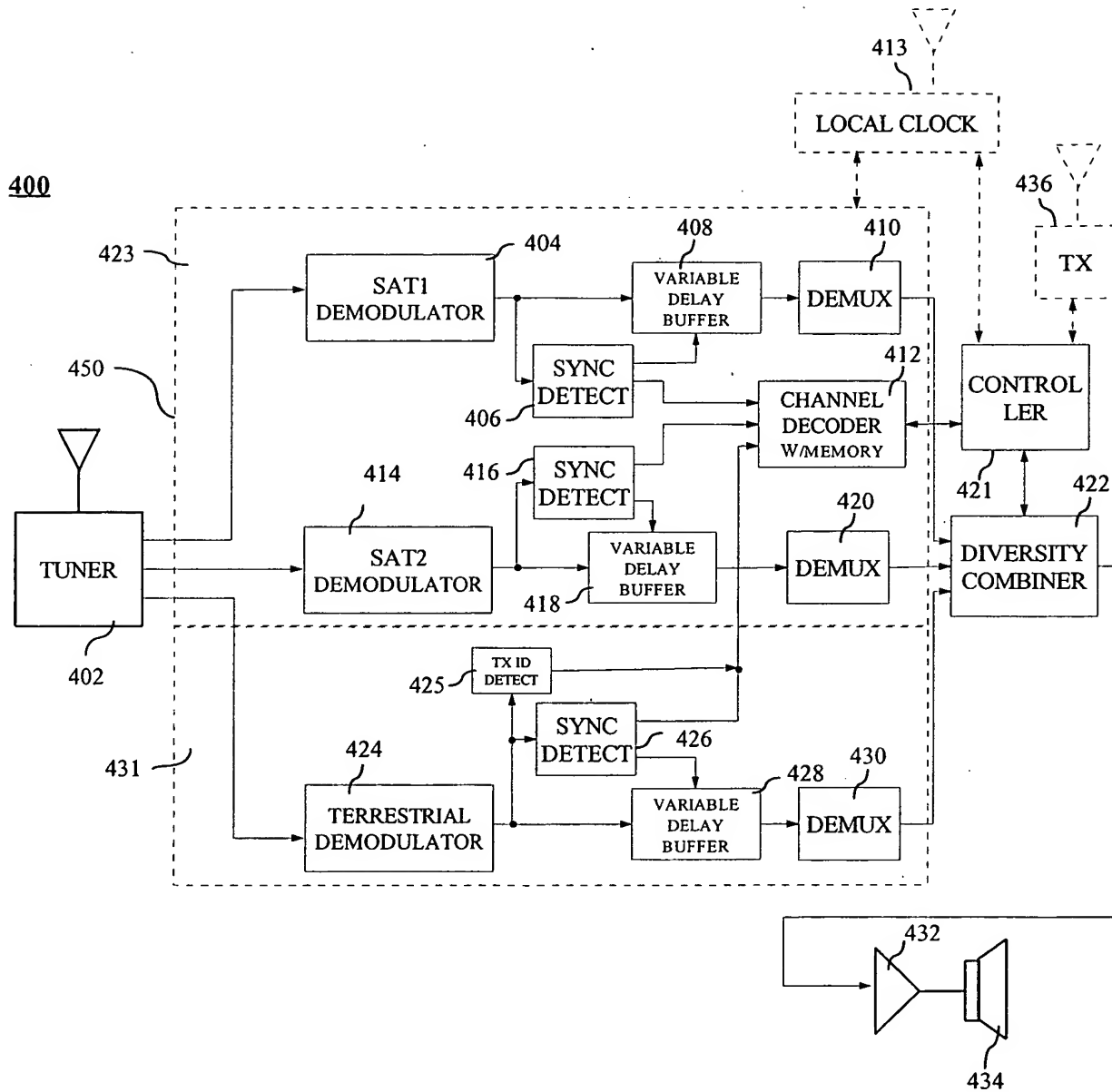


FIG. 5

SYNC PULSE OUTPUT TIMING FROM SATELLITE & TERRESTRIAL ARMS

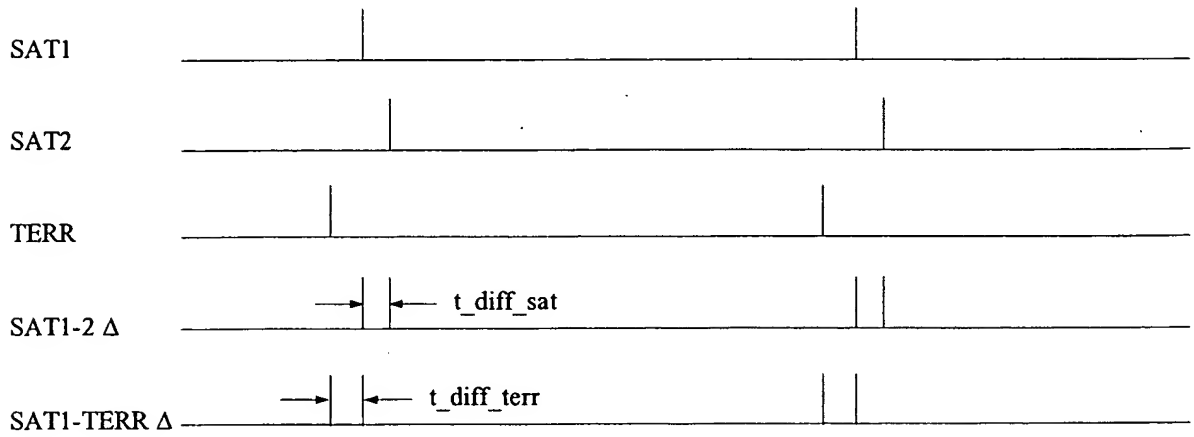


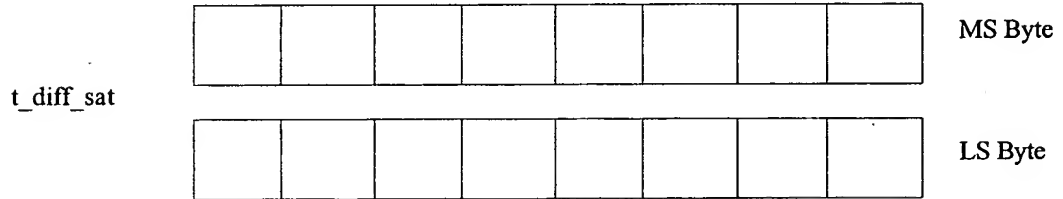
FIG. 6

CHANNEL DECODER RADIO LOCATION REGISTERS



X = 0 Sat1 early or no offset

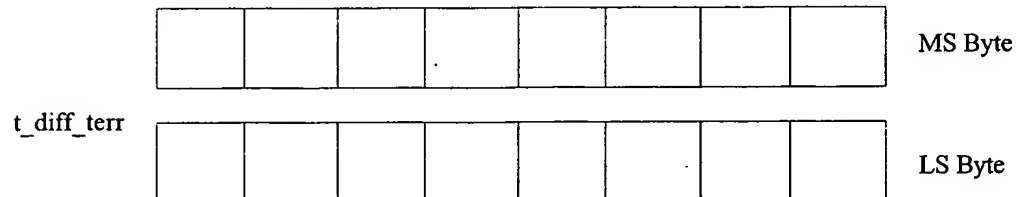
X = 1 Sat2 early



0000 hex = no offset

XXXX hex = Total 5.98 MHz clock cycles between Sat1 & Sat2 pulses

FFFF hex = Both Sat Sync Pulses not present



0000 hex = no offset

XXXX hex = Total 1.495 MHz clock cycles less 20 mS or 29,900 cycles between Terr & Sat1 Sync pulses

FFFF hex = Sat1 and/or Terr Sync Pulse not present

FIG. 7

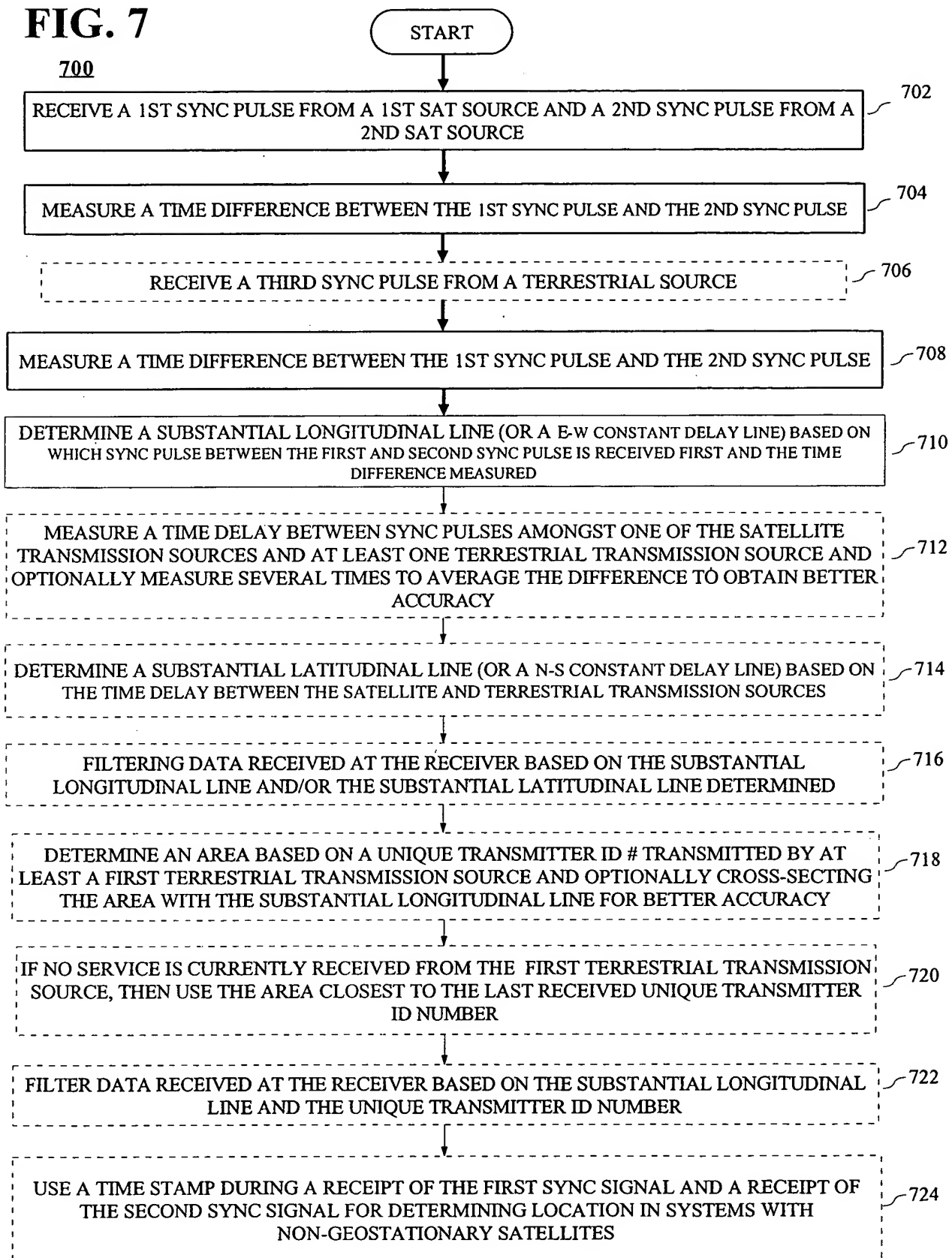


FIG. 7